KiwiSDR: Beagle P8/P9 GPIO pins that are okay for user 3.3V applications

"kiwi" column in table below

e	Also wired to FPGA input, but okay as Beagle input or output
ok	Okay as Beagle input or output
(others)	Do not use any others

"GPIO mode 7" column in table below

3_19	For example "3_19" means GPIO 3, bit 19 (of GPIO 0-3, bit 0-31)

P9 L

P9 L	beagle	GPIO mode 7	kiwi
1	gnd		gnd
2	gnd		gnd
3	3.3v		3.3v
4	3.3v		3.3v
5	vdd_5v ext		5EXT
6	vdd_5v ext		5EXT
7	sys_5v		5INT
8	sys_5v		5INT
9	pwr_but		x
10	sys_reset_n		x
11	gpmc	0_30	e
12	gpmc	1_28	pgm
13	gpmc	0_31	e
14	gpmc	1_18	init
15	gpmc	1_16 2_0	e
16	gpmc	1_19	cs1
17	spi	0_5	cs0
18	spi	0_4	mosi
19	config	0_13	config
20	config	0_12	config
21	spi	0_3	miso
22	spi	0_2	sclk
23	gpmc	1_17	e
24	gpmc	0_15	e / intr
25	HDMI a-clk	3_21	x
26		0_14	e
27		3_19	ok
28	HDMI	3_17	x
29	HDMI	3_15	x
30		3_16	x
31	HDMI	3_14	x
32			x
33	analog		x
34			x
35			x
36	analog		x
37			x
38			x
39			x
40			x
41	multi	0_20 3_20	x
42	multi	0_7 3_18	x
43	gnd		x
44	gnd		x
45	gnd		x
46	gnd		x
P9 L		m7	kiwi

P8 R

P8 R	beagle	GPIO mode 7	kiwi
1	gnd		x
2	gnd		x
3	gpmc	1_6	x
4	emmc	1_7	x
5	emmc	1_2	x
6	emmc	1_3	x
7		2_2	tdi
8		2_3	tdo
9		2_5	tck
10		2_4	tms
11	gpmc	1_13	e
12		1_12	e
13		0_23	e
14		0_26	e
15	gpmc	1_15	e
16		1_14	e
17		0_27	e
18		2_1	e
19		0_22	e
20	emmc	1_31	x
21		1_30	x
22	emmc	1_5	x
23		1_4	x
24		1_1	x
25		1_0	x
26		1_29	e
27	lcd HDMI	2_22	x
28		2_24	x
29		2_23	x
30		2_25	x
31		0_10	x
32		0_11	x
33		0_9	x
34		2_17	x
35		0_8	x
36	lcd HDMI	2_16	x
37		2_14	x
38	sysboot	2_15	x
39		2_12	x
40		2_13	x
41		2_10	x
42		2_11	x
43		2_8	x
44		2_9	x
45		2_6	x
46		2_7	x
P8 R		m7	kiwi