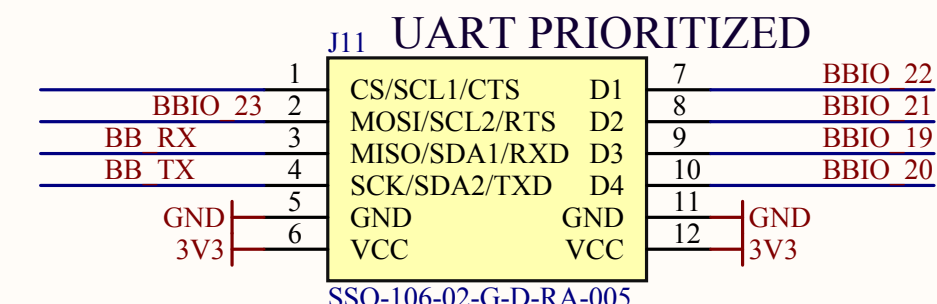
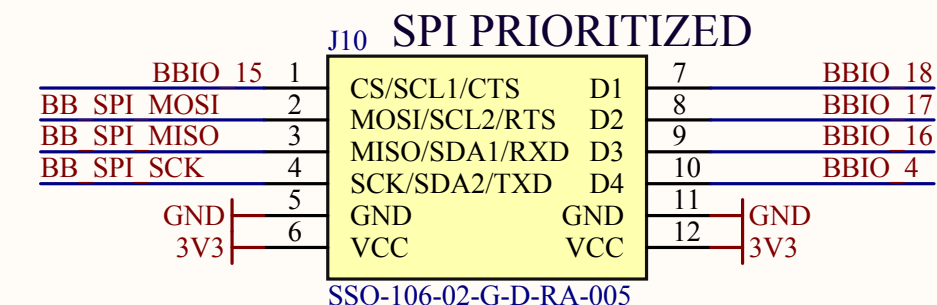
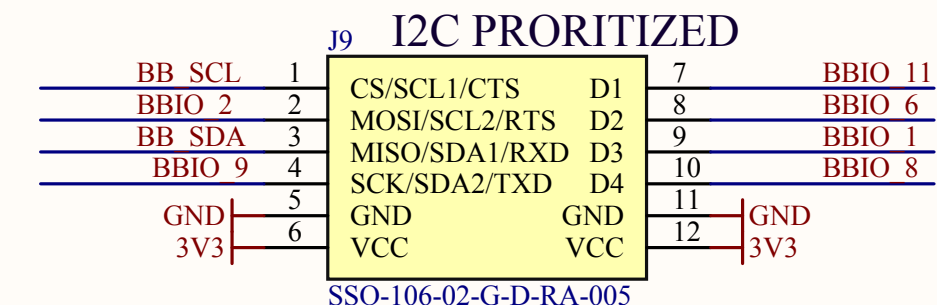
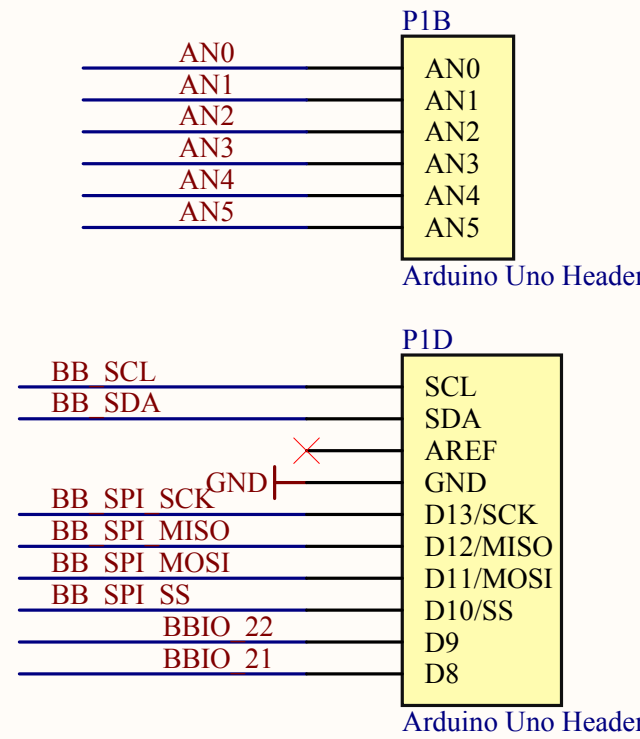
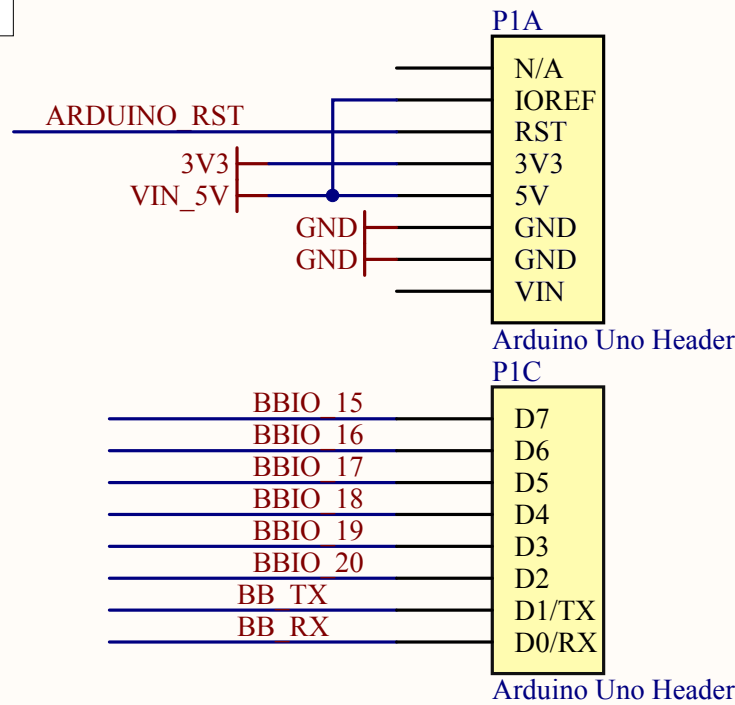
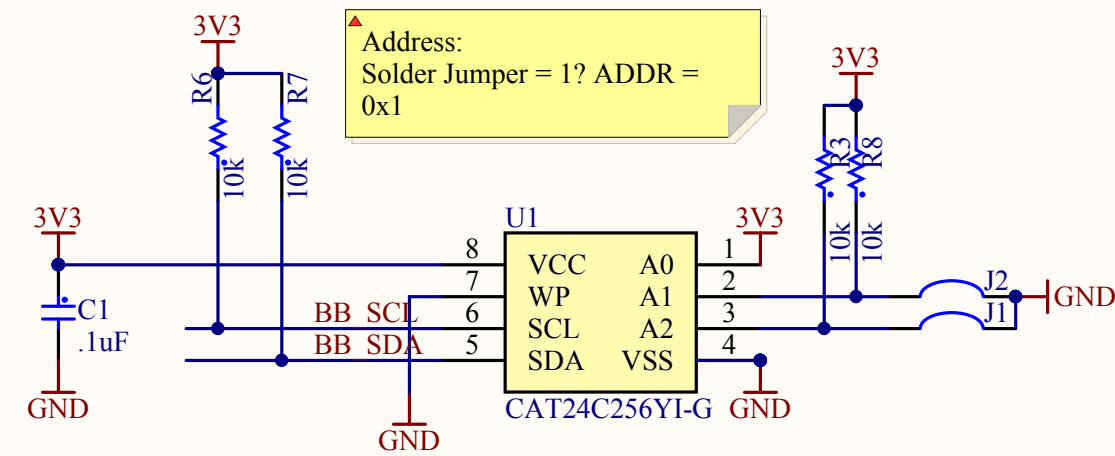
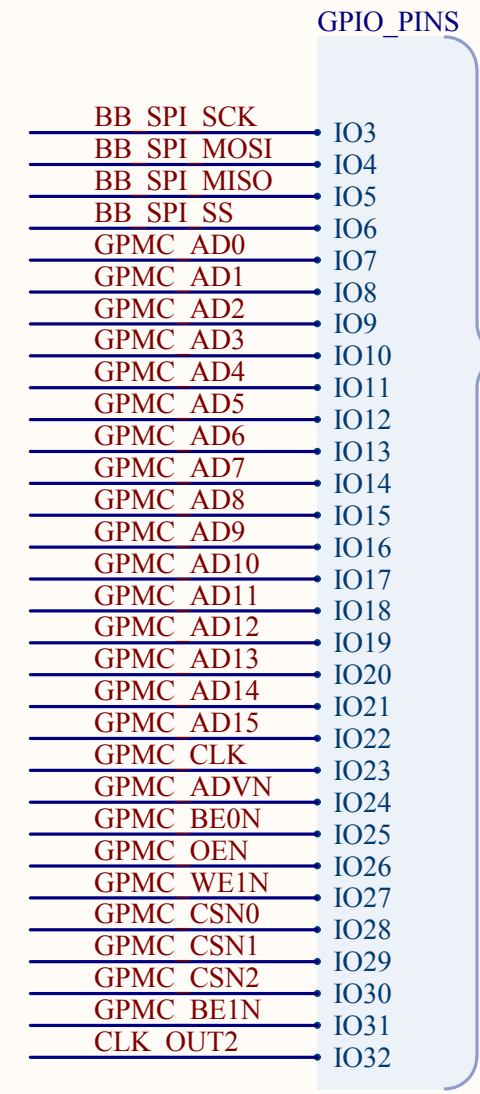
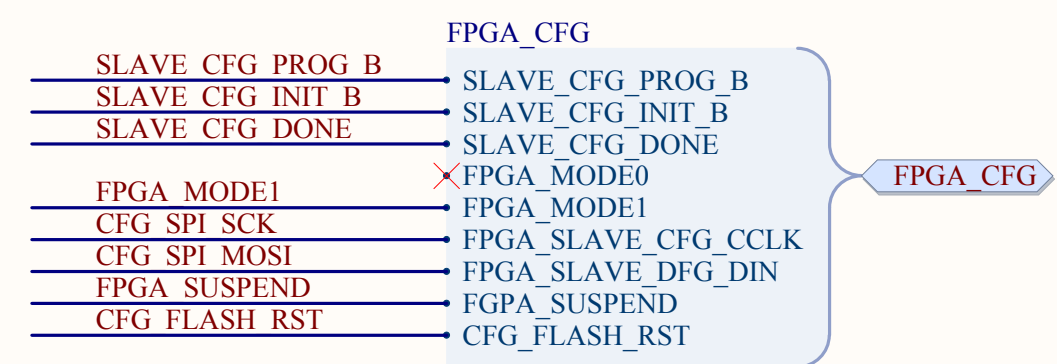


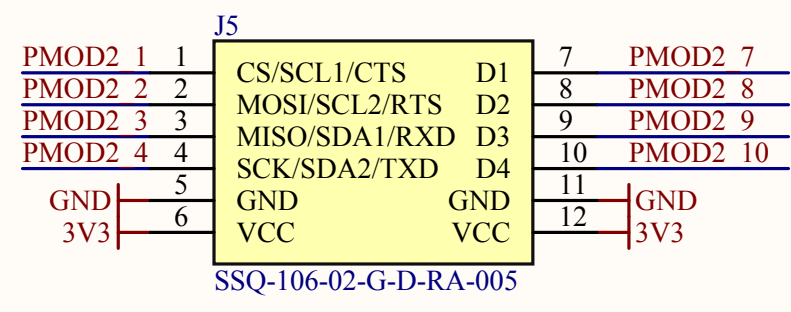
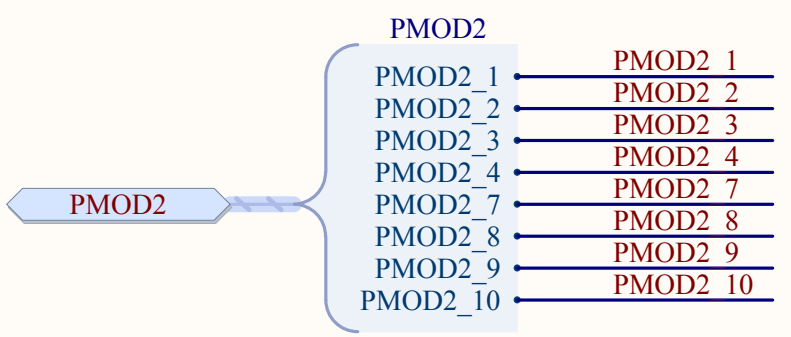
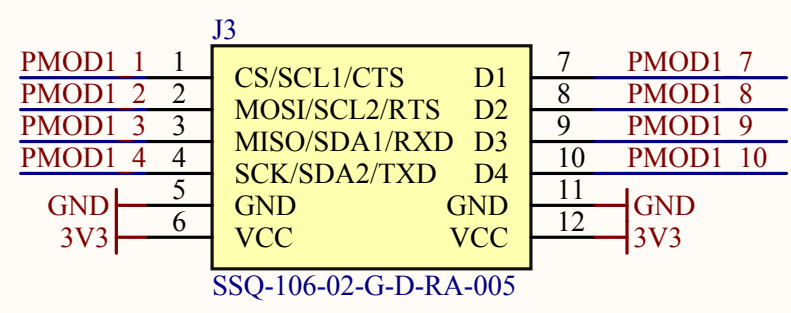
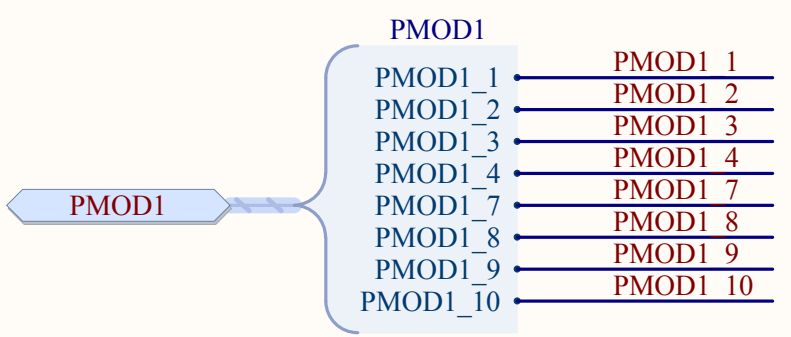
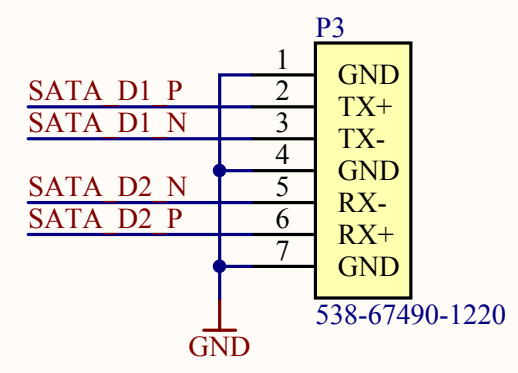
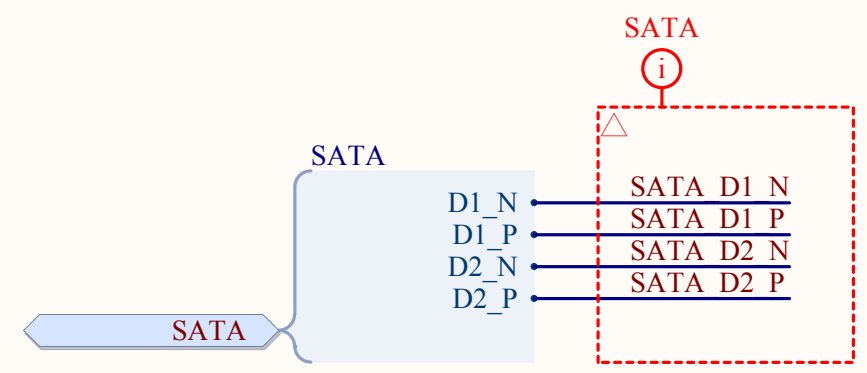
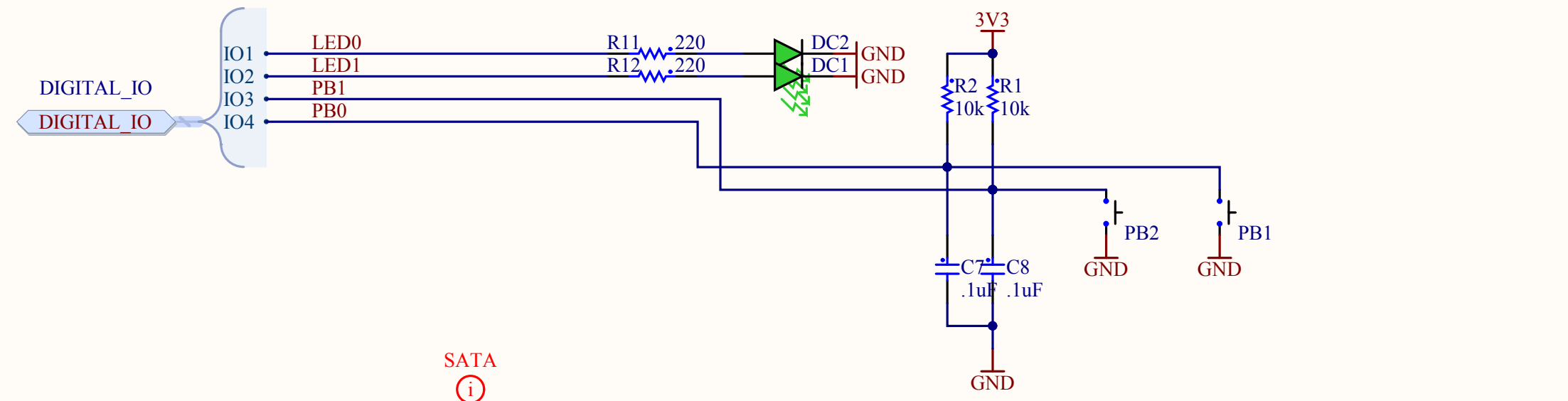
Beaglebone IO



1	GND	2	GND	3	GND	4	GND	5	GND	6	GND	7	GND	8	GND	9	GND	10	GND	11	GND	12	GND	13	GND	14	GND	15	GND	16	GND	17	GND	18	GND	19	GND	20	GND	21	GND	22	GND	23	GND	24	GND	25	GND	26	GND	27	GND	28	GND	29	GND	30	GND	31	GND	32	GND	33	GND	34	GND	35	GND	36	GND	37	GND	38	GND	39	GND	40	GND	41	GND	42	GND	43	GND	44	GND	45	GND	46	GND	47	GND	48	GND	49	GND	50	GND	51	GND	52	GND	53	GND	54	GND	55	GND	56	GND	57	GND	58	GND	59	GND	60	GND	61	GND	62	GND	63	GND	64	GND	65	GND	66	GND	67	GND	68	GND	69	GND	70	GND	71	GND	72	GND	73	GND	74	GND	75	GND	76	GND	77	GND	78	GND	79	GND	80	GND	81	GND	82	GND	83	GND	84	GND	85	GND	86	GND	87	GND	88	GND	89	GND	90	GND	91	GND	92	GND	93	GND	94	GND	95	GND	96	GND	97	GND	98	GND	99	GND	100	GND
---	-----	---	-----	---	-----	---	-----	---	-----	---	-----	---	-----	---	-----	---	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	----	-----	-----	-----

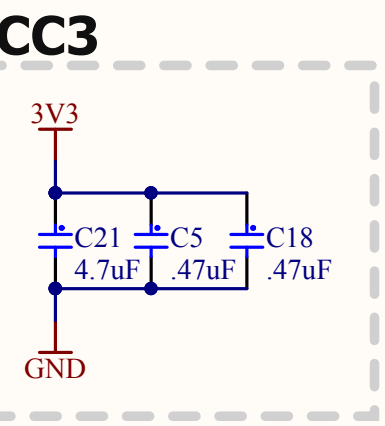
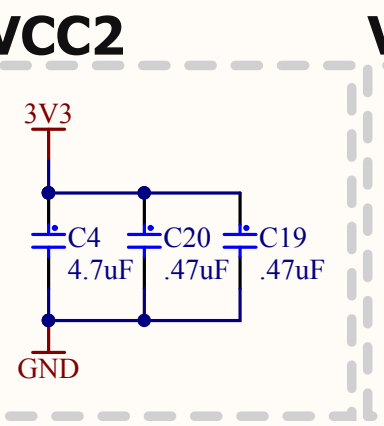
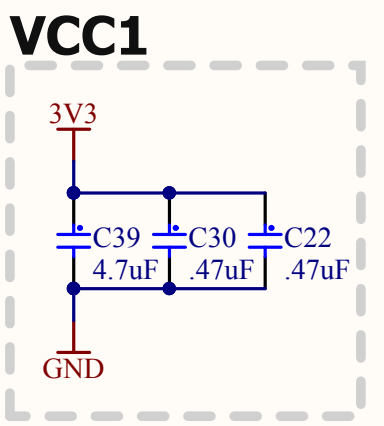
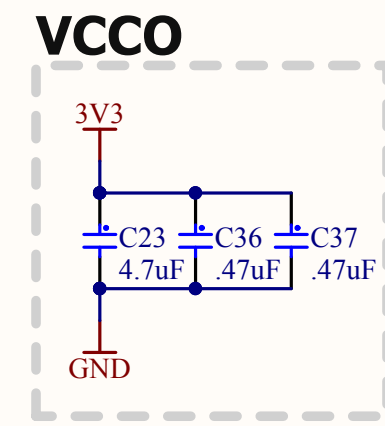
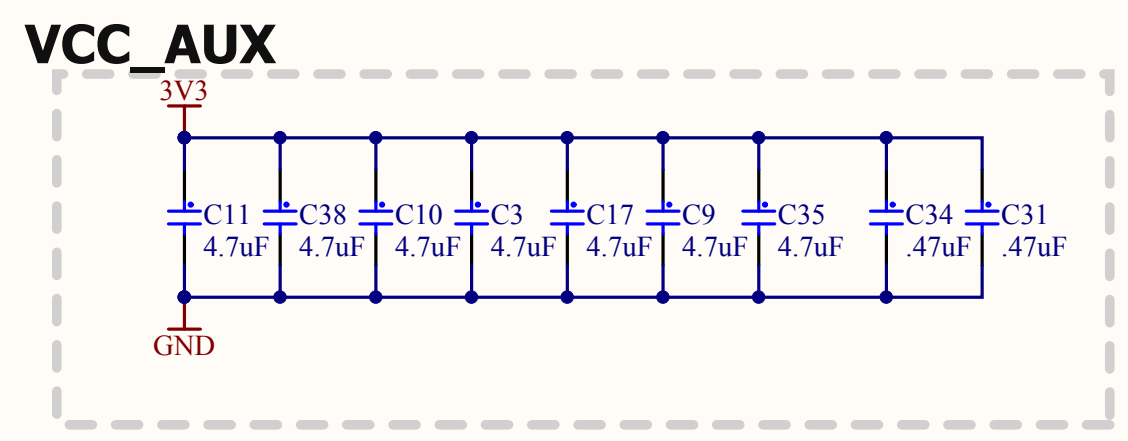
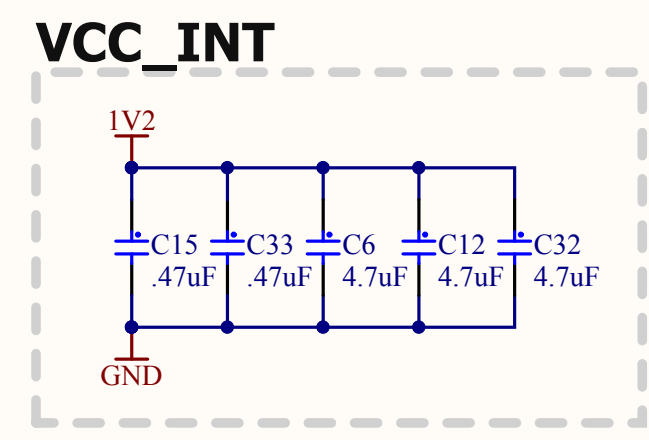
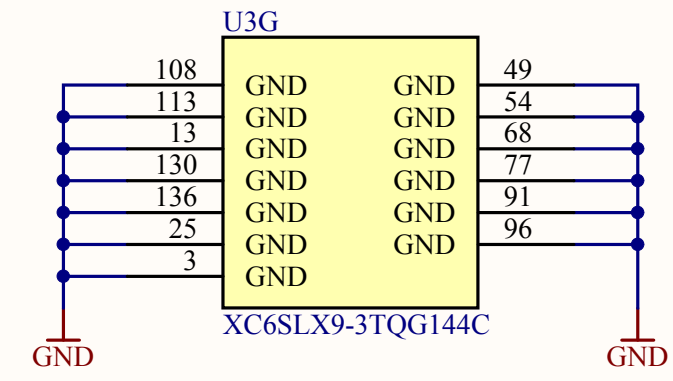
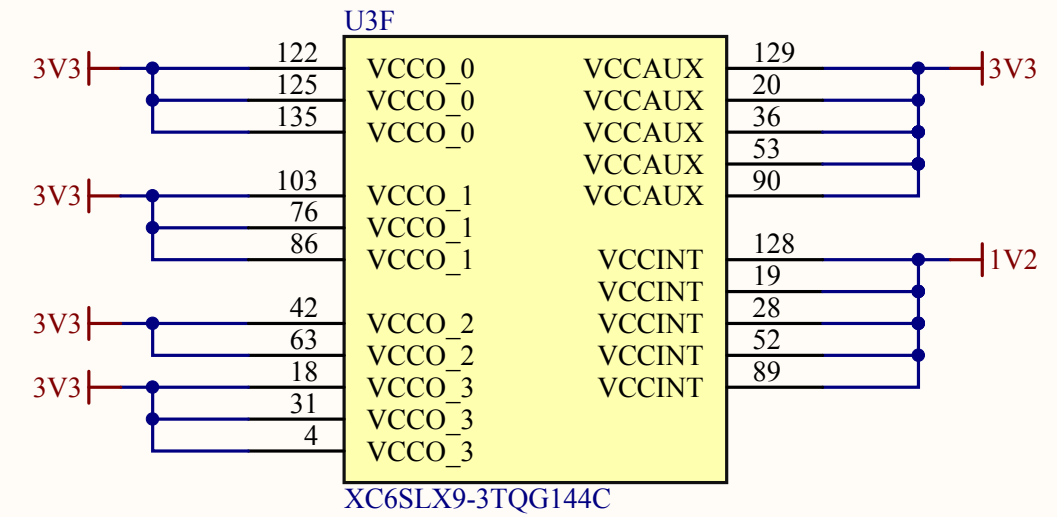


FPGA IO

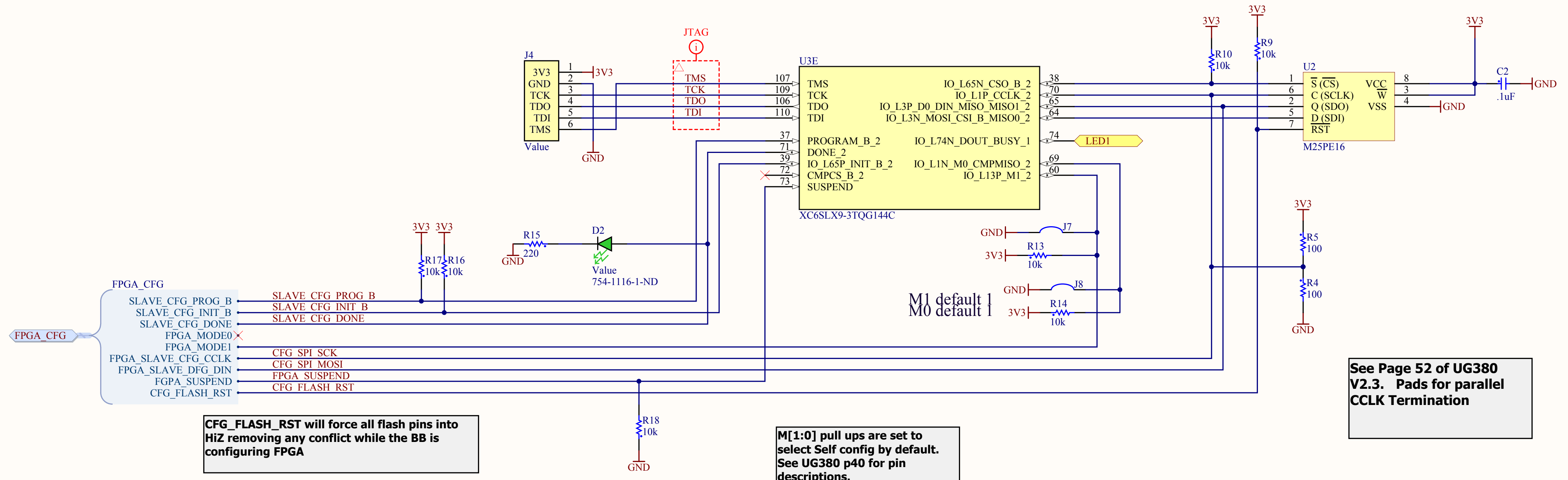


Title	Digital IO	
Revision:	RA.1	Sheet 1 of 1
Date:	12/29/2012	Engineer: M. Jones





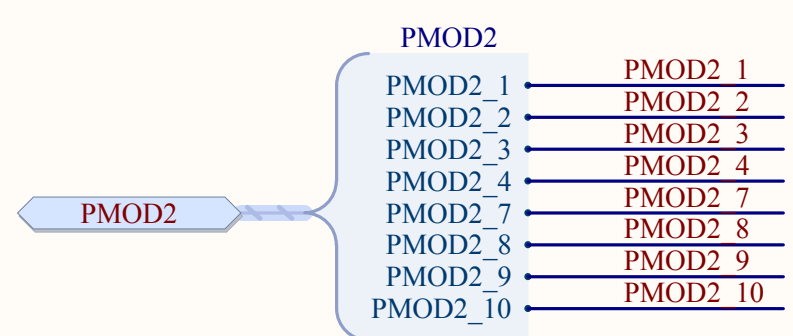
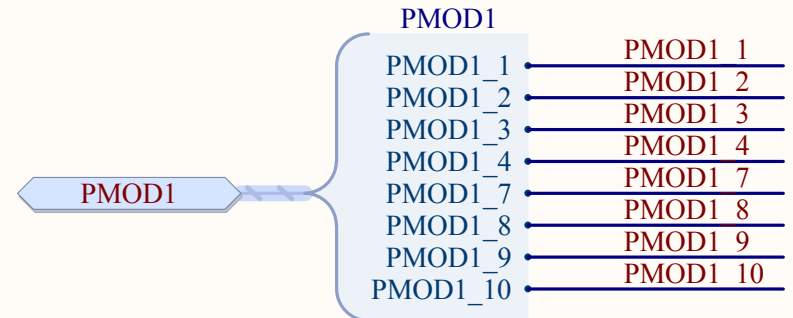
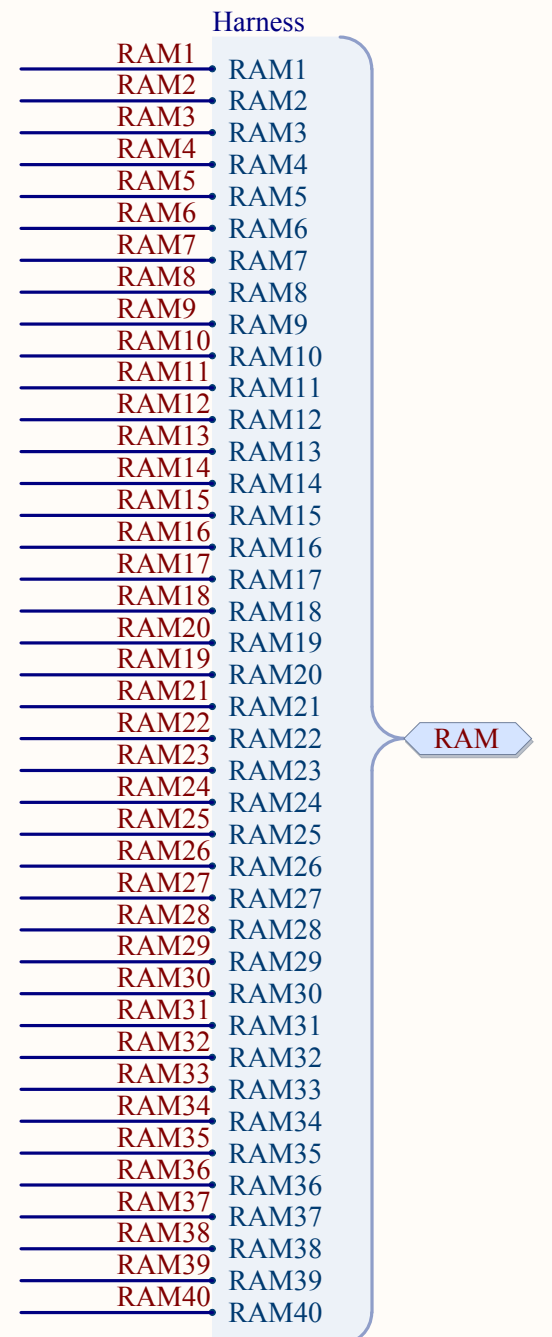
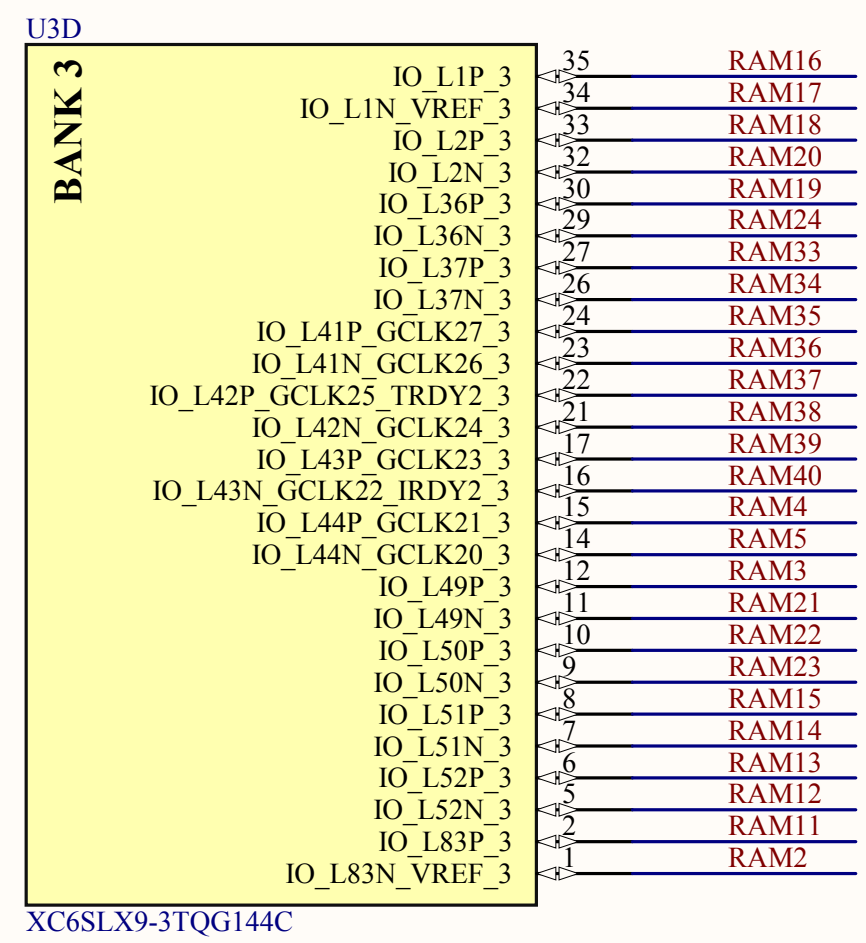
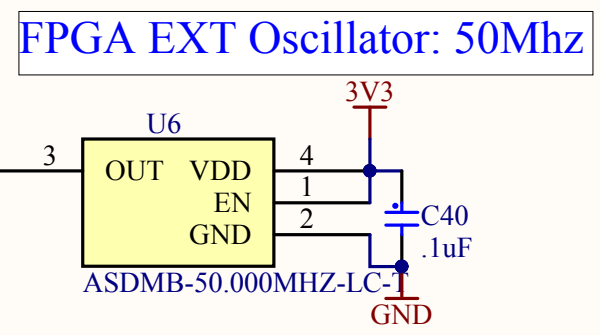
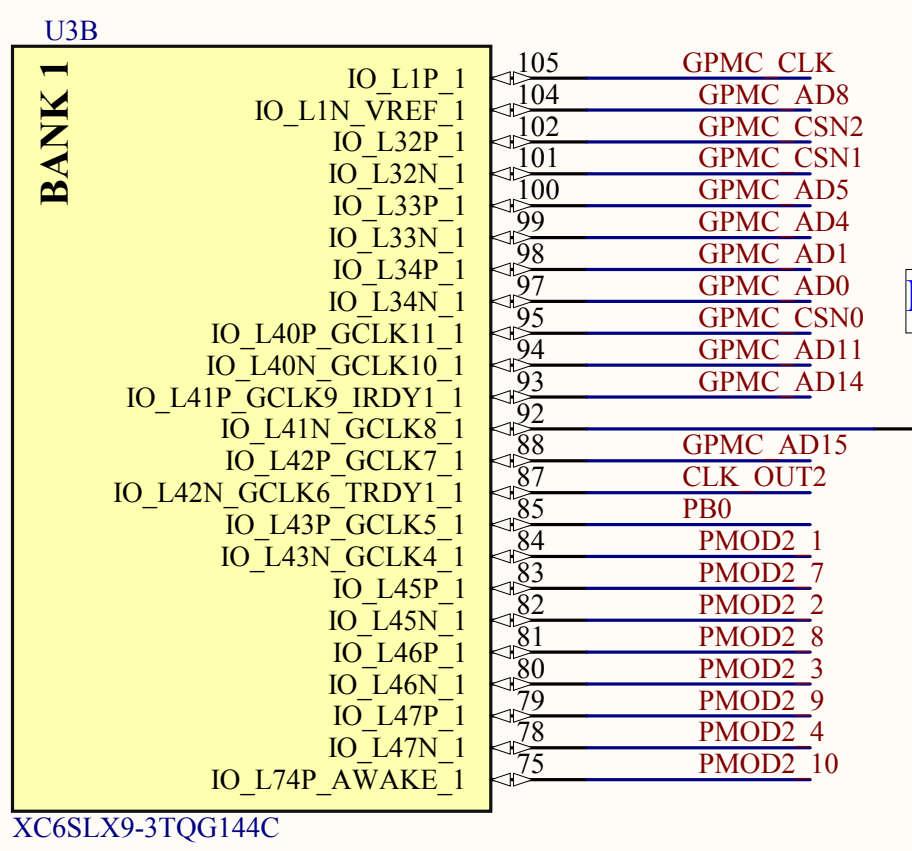
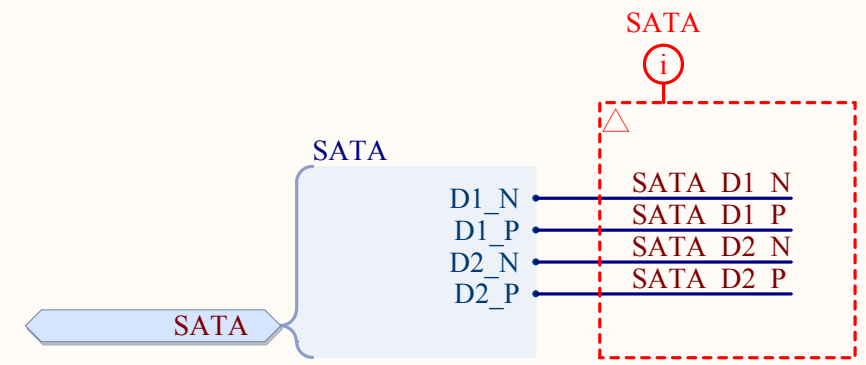
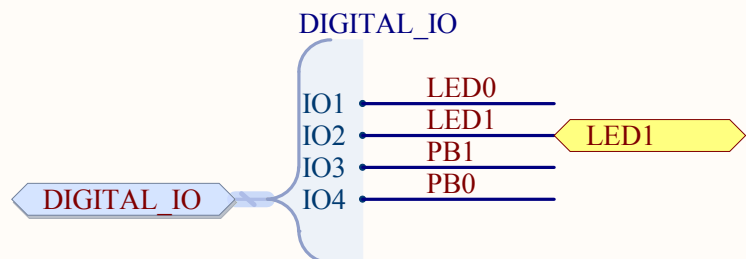
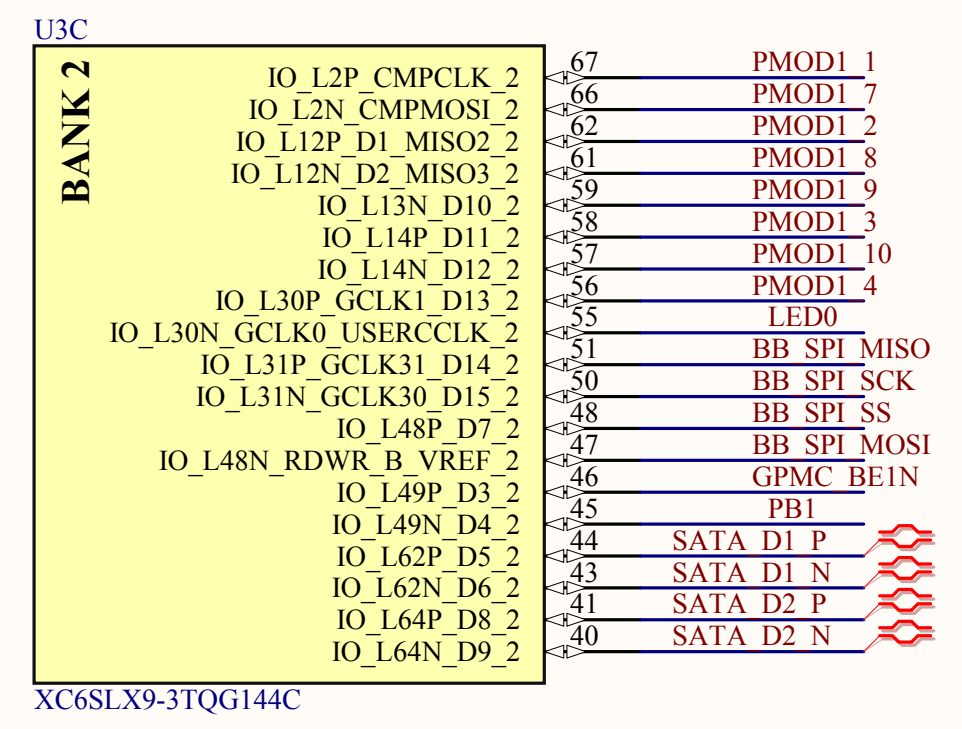
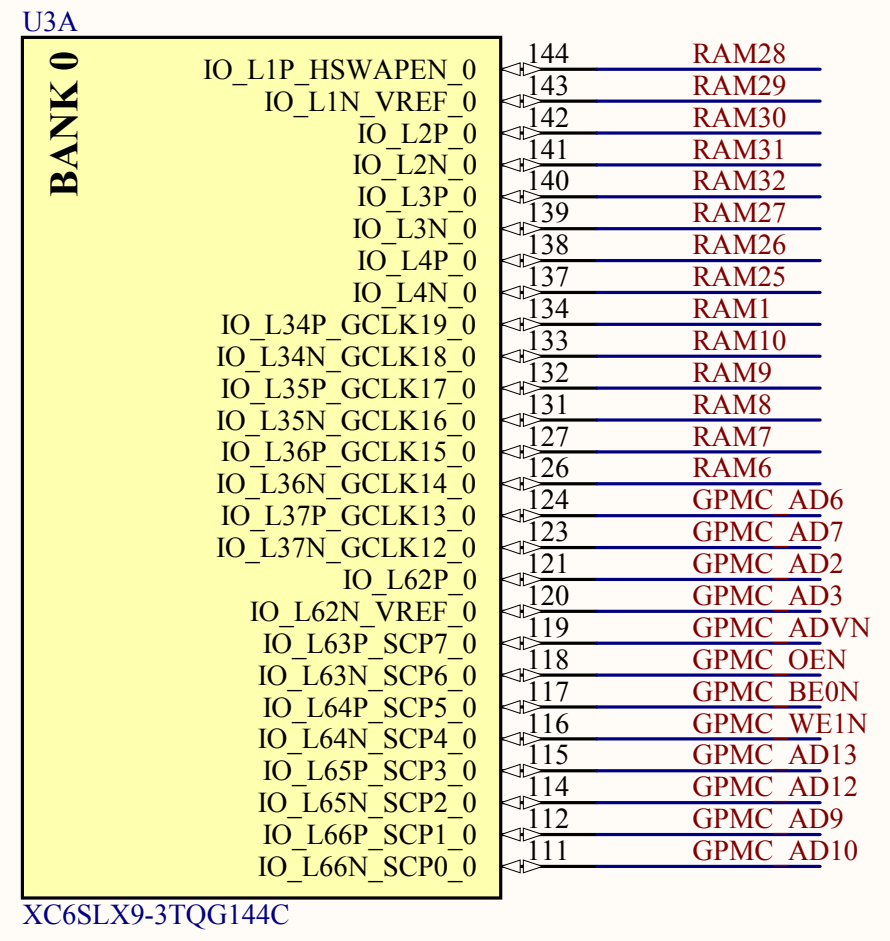
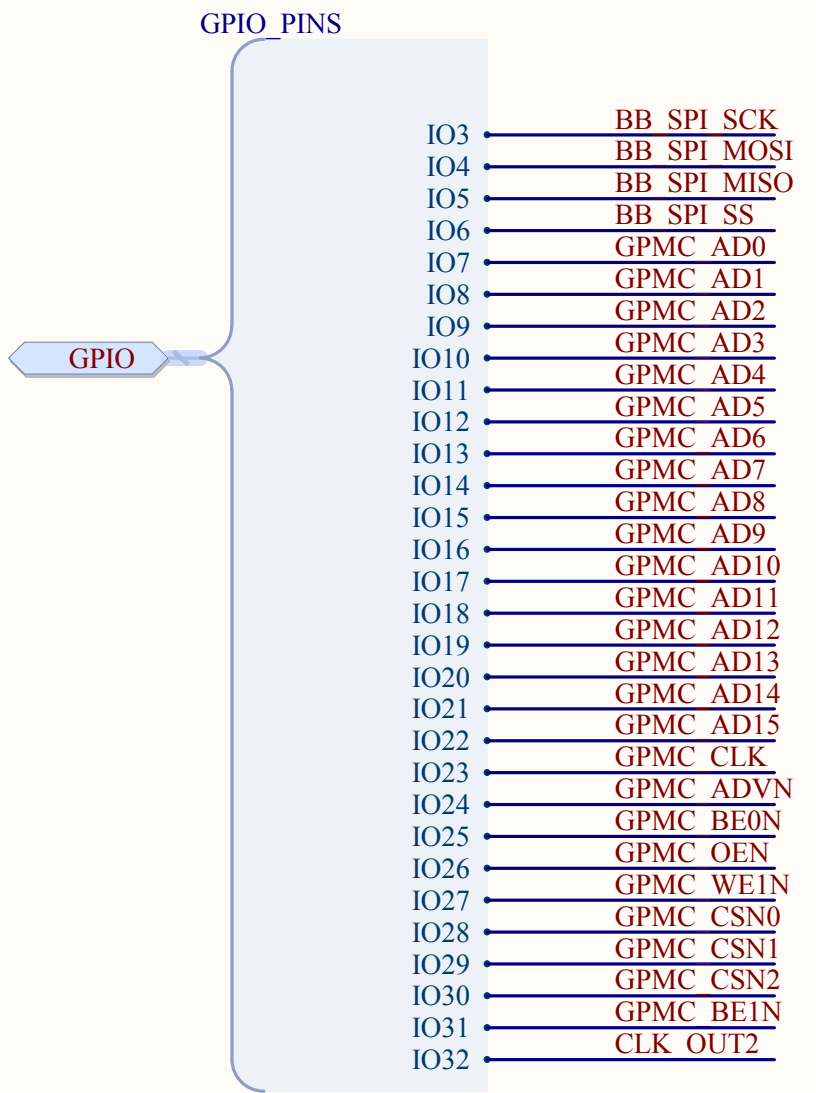
Decoupling cap configuration recommended by Xilinx UG393 (V1.2) for Spartan 6 power distribution



CFG_FLASH_RST will force all flash pins into HiZ removing any conflict while the BB is configuring FPGA

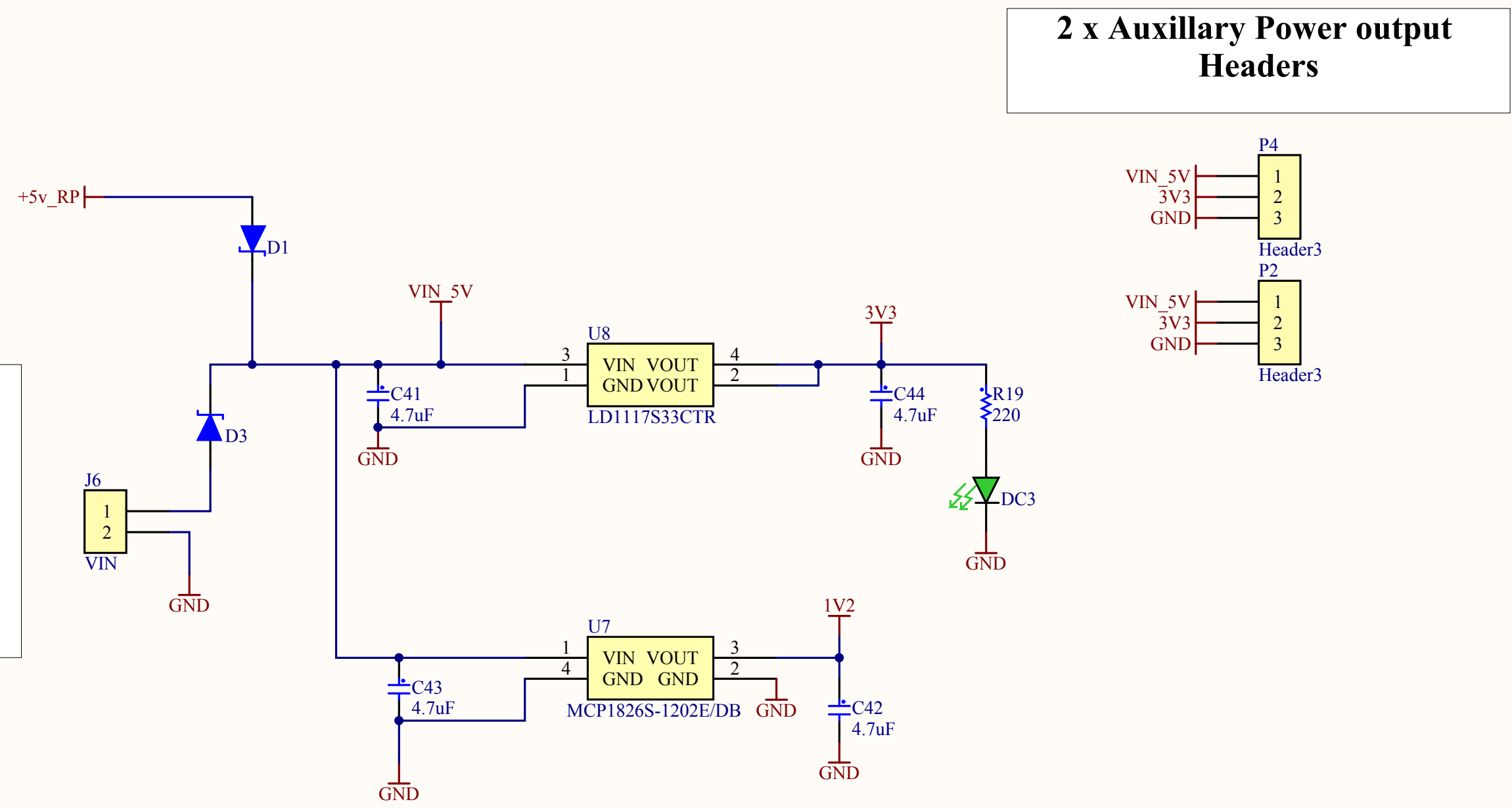
M[1:0] pull ups are set to select Self config by default. See UG380 p40 for pin descriptions.
01 = SelfConfig
11 = SlaveConfig

See Page 52 of UG380 V2.3. Pads for parallel CCLK Termination



Power: By Default Power will be supplied by the Beaglebone.

Optionally power can be supplied through FPGA VIN header.



Logo1
Valent Logo 750
Logo2
Valent Logo 1000

SRAM

SDRAM

